

August 12, 1999
(Case No.: RD-036)

Commissioner of Patents and Trademarks
Washington, D.C. 20231

08/12/99
1669 U.S. PTO

628273/60
08/21/99

Sir:

Transmitted herewith for filing under 37 CFR § 1.53(b) is the application of:

Inventors: **Stefanos Sidiropoulos and Joe-Anand Louis-Chandran**

For: **Integrated Circuit Device having I/O Structures with Reduced Input Loss**

including:

- [XX] Specification (30 consecutively numbered pages and 7 sheets of 1 set of formal drawings).
- [XX] Declaration and Power of Attorney (2 pages).

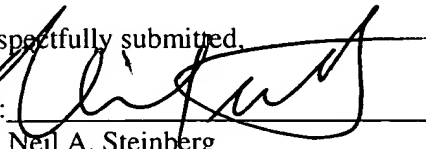
Address all future communications to: Neil A. Steinberg
Rambus Inc.
2465 Latham Street
Mountain View, California
Telephone: 650-944-7772 Facsimile: 650-944-8080

FEE CALCULATION

Basic Fee	\$ <u>760.00</u>
Additional Fees:	
Surcharge for more that 20 total claims	\$ <u>144.00</u>
Surcharge for more than 3 independent claims	\$ <u>- 0 -</u>
Surcharge for multiple dependent claims	\$ <u>- 0 -</u>
Total Filing Fee	\$ <u>904.00</u>

- [] A check payable to the Commissioner of Patents and Trademarks, in the amount of \$ _____, is enclosed as payment for the above fees.
- [XX] Please charge my Deposit Account No. 50-0998 in the amount of \$904.00 to cover the above fees. A duplicate copy of this sheet is enclosed.
- [XX] The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 50-0998. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

By: 
Neil A. Steinberg
Reg. No. 34,735
650-944-7772